

3-V TO 5.5-V MULTICHANNEL RS-232 1-Mbit/s LINE DRIVER/RECEIVER

Check for Samples: [TRS3237E](#)

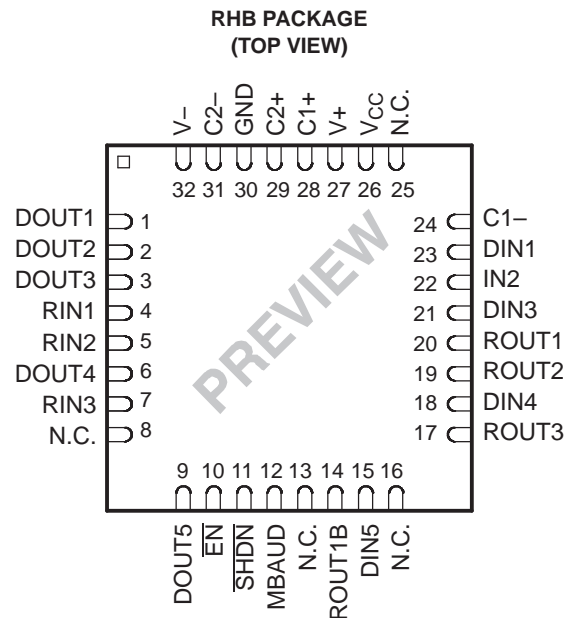
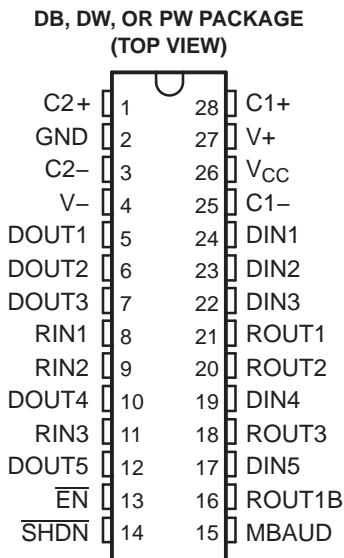
FEATURES

- Meets or Exceeds the Requirements of TIA/EIA-232-F and ITU v.28 Standards
- Operates With 3-V to 5.5-V V_{CC} Supply
- Operates From 250 kbits/s to 1 Mbit/s
- Low Standby Current . . . 1 μ A Typical
- External Capacitors . . . 4 \times 0.1 μ F
- Accepts 5-V Logic Input With 3.3-V Supply
- Designed to Be Interchangeable With Industry Standard '3237E Devices
- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II

- ESD Protection for RS-232 I/O Pins
 - ± 15 kV – Human-Body Model (HBM)
 - ± 8 kV – IEC61000-4-2, Contact Discharge
 - ± 15 kV – IEC61000-4-2, Air-Gap Discharge

APPLICATIONS

- Battery-Powered, Hand-Held, and Portable Equipment
- PDAs and Palmtop PCs
- Notebooks, Sub-Notebooks, and Laptops
- Digital Cameras
- Mobile Phones and Wireless Devices



N.C.— Not internally connected

DESCRIPTION/ORDERING INFORMATION

The TRS3237E consists of five line drivers, three line receivers, and a dual charge-pump circuit with ± 15 -kV ESD protection pin to pin (serial-port connection pins, including GND). The device meets the requirements of TIA/EIA-232-F and provides the electrical interface between an asynchronous communication controller and the serial-port connector. The charge pump and four small external capacitors allow operation from a single 3-V to 5.5-V supply. This device operates at data signaling rates of 250 kbit/s in normal operating mode (MBAUD = GND) and 1Mbit/s when MBAUD = V_{CC} . The driver output slew rate is a maximum of 30 V/ μ s.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

The TRS3237E transmitters are disabled and the outputs are forced into high-impedance state when the device is in shutdown mode ($\overline{\text{SHDN}} = \text{GND}$) and the supply current falls to less than 1 μA . Also, during shutdown, the onboard charge pump is disabled; V_+ is lowered to V_{CC} , and V_- is raised toward GND. Receiver outputs also can be placed in the high-impedance state by setting enable ($\overline{\text{EN}}$) high. ROUT1B remains active all the time, regardless of the EN and SHDN condition.

The TRS3237EC is characterized for operation from 0°C to 70°C. The TRS3237EI is characterized for operation from –40°C to 85°C.

Table 1. ORDERING INFORMATION⁽¹⁾

| T_A | PACKAGE ⁽²⁾ | | ORDERABLE PART NUMBER | TOP-SIDE MARKING |
|---------------|------------------------|--------------|-----------------------|------------------|
| 0°C to 70°C | SSOP – DB | Reel of 2000 | TRS3237ECDBR | TRS3237EC |
| | SOIC – DW | Reel of 2000 | TRS3237ECDWR | TRS3237EC |
| | TSSOP – PW | Reel of 2000 | TRS3237ECPWR | RS37EC |
| | QFN – RHB | Reel of 2000 | TRS3237ECRHBR | PREVIEW |
| –40°C to 85°C | SSOP – DB | Reel of 2000 | TRS3237EIDBR | TRS3237EI |
| | SOIC – DW | Reel of 2000 | TRS3237EIDWR | TRS3237EI |
| | TSSOP – PW | Reel of 2000 | TRS3237EIPWR | RS37EI |
| | QFN – RHB | Reel of 2000 | TRS3237EIRHBR | PREVIEW |

(1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.

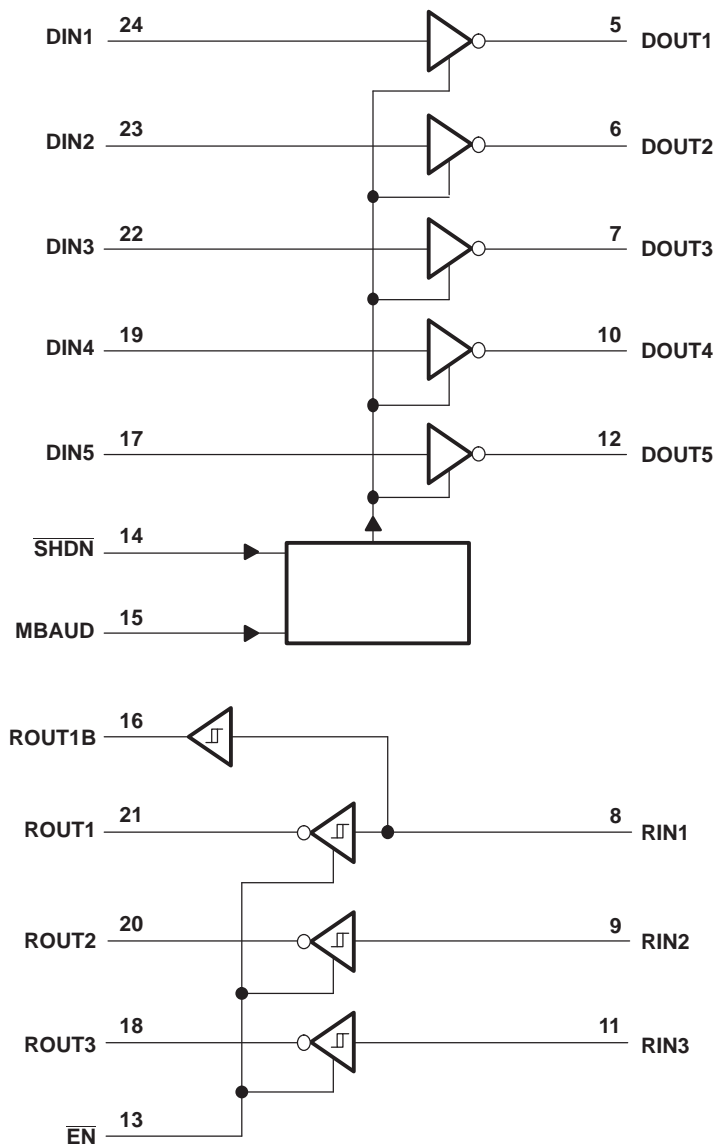
(2) Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.

Table 2. FUNCTION TABLE

| INPUTS | | OUTPUTS | | |
|--------------------------|------------------------|------------------|------------------|--------|
| $\overline{\text{SHDN}}$ | $\overline{\text{EN}}$ | DOUT | ROUT | ROUT1B |
| 0 | 0 | Z ⁽¹⁾ | Active | Active |
| 0 | 1 | Z ⁽¹⁾ | Z ⁽¹⁾ | Active |
| 1 | 0 | Active | Active | Active |
| 1 | 1 | Active | Z ⁽¹⁾ | Active |

(1) Z = high impedance (off)

LOGIC DIAGRAM (POSITIVE LOGIC)



ABSOLUTE MAXIMUM RATINGS⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

| | | | MIN | MAX | UNIT |
|------------------|---|---|-----------|-----------------------|------|
| V _{CC} | Supply voltage range ⁽²⁾ | | −0.3 | 6 | V |
| V+ | Positive-output supply voltage range ⁽²⁾ | | −0.3 | 7 | V |
| V− | Negative-output supply voltage range ⁽²⁾ | | 0.3 | −7 | V |
| V+ − V− | Supply voltage difference ⁽²⁾ | | | 13 | V |
| V _I | Input voltage range | Driver ($\overline{\text{SHDN}}$, MBAUD, $\overline{\text{EN}}$) | −0.3 | 6 | V |
| | | Receiver | −25 | 25 | |
| V _O | Output voltage range | Driver | −13.2 | 13.2 | V |
| | | Receiver | −0.3 | V _{CC} + 0.3 | |
| | Short-circuit duration | DOUT to GND | Unlimited | | |
| θ _{JA} | Package thermal impedance ⁽³⁾ | | | 62 | °C/W |
| T _{std} | Storage temperature range | | −65 | 150 | °C |

- (1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) All voltages are with respect to network GND.
- (3) The package thermal impedance is calculated in accordance with JESD 51-7.

RECOMMENDED OPERATING CONDITIONS⁽¹⁾

See [Figure 5](#)

| | | MIN | NOM | MAX | UNIT |
|-----------------|---|---|-----|-----|------|
| Supply voltage | V _{CC} = 3.3 V | 3 | 3.3 | 3.6 | V |
| | V _{CC} = 5 V | 4.5 | 5 | 5.5 | |
| V _{IH} | Driver and control high-level input voltage | DIN, $\overline{\text{SHDN}}$, MBAUD, $\overline{\text{EN}}$ | | 2 | V |
| | | | | 5.5 | |
| V _{IL} | Driver and control low-level input voltage | DIN, $\overline{\text{SHDN}}$, MBAUD, $\overline{\text{EN}}$ | | 2.4 | V |
| | | | | 5.5 | |
| V _I | Receiver input voltage | –25 | | 25 | V |
| T _A | Operating free-air temperature | TRS3237EC | | 0 | °C |
| | | TRS3237EI | | –40 | |

- (1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3 V to 5 V.

ELECTRICAL CHARACTERISTICS⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see [Figure 5](#))

| PARAMETER | TEST CONDITIONS | MIN | TYP ⁽²⁾ | MAX | UNIT |
|-----------------|---|---|--------------------|-----|------------|
| I _I | Input leakage current | DIN, $\overline{\text{SHDN}}$, MBAUD, $\overline{\text{EN}}$ | | 9 | 18 μ A |
| I _{CC} | Supply current (T _A = 25°C) | No load, $\overline{\text{SHDN}} = \text{V}_{\text{CC}}$ | | 0.5 | 2 mA |
| | | $\overline{\text{SHDN}} = \text{GND}$ | | 1 | 10 μ A |
| | | Shutdown supply current $\overline{\text{SHDN}} = \text{RIN} = \text{GND}$, DIN = GND or V _{CC} | | 10 | 300 nA |

- (1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3 V to 5 V.
- (2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C.

DRIVER SECTION ELECTRICAL CHARACTERISTICS⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see [Figure 5](#))

| PARAMETER | TEST CONDITIONS | MIN | TYP ⁽²⁾ | MAX | UNIT |
|---|--|-----|--------------------|-----|------|
| V _{OH} High-level output voltage | DOUT at R _L = 3 kΩ to GND, DIN = GND | 5 | 5.4 | | V |
| V _{OL} Low-level output voltage | DOUT at R _L = 3 kΩ to GND, DIN = V _{CC} | –5 | –5.4 | | V |
| I _{IH} High-level input current | V _I = V _{CC} | | ±0.01 | ±1 | μA |
| I _{IL} Low-level input current | V _I at GND | | ±0.01 | ±1 | μA |
| I _{OS} Short-circuit output current ⁽³⁾ | V _{CC} = 3.6 V or 3.3 V, V _O = 0 V | | | ±60 | mA |
| r _o Output resistance | V _{CC} , V ₊ , and V _– = 0 V, V _O = ±2 V | 300 | 50k | | Ω |

(1) Test conditions are C1–C4 = 0.1 μF at V_{CC} = 3 V to 5 V.

(2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C.

(3) Short-circuit durations should be controlled to prevent exceeding the device absolute power-dissipation ratings, and not more than one output should be shorted at a time.

DRIVER SECTION SWITCHING CHARACTERISTICS⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see [Figure 5](#))

| PARAMETER | | TEST CONDITIONS | | MIN | TYP ⁽²⁾ | MAX | UNIT |
|--------------------|---|--|-------------------------------------|-------------------------|--------------------|--------|------|
| Maximum data rate | C _L = 1000 pF, MBAUD = GND | R _L = 3 kΩ, 1 DIN switching, See Figure 1 | | 250 | | kbit/s | |
| | C _L = 1000 pF, V _{CC} = 4.5 V to 5.5 V, MBAUD = V _{CC} | | 1000 | | | | |
| | C _L = 250 pF, V _{CC} = 3 V to 4.5 V, MBAUD = V _{CC} | | 1000 | | | | |
| t _{sk(p)} | Pulse skew ⁽³⁾ | C _L = 150 pF to 2500 pF, R _L = 3 kΩ to 7 kΩ, MBAUD = V _{CC} or GND, See Figure 2 | | 100 | | ns | |
| SR(tr) | Slew rate, transition region (see Figure 1) | V _{CC} = 3.3 V, R _L = 3 kΩ to 7 kΩ, T _A = 25°C | C _L = 150 pF to 1000 pF | MBAUD = GND | 6 | 30 | V/μs |
| | | | | MBAUD = V _{CC} | 24 | 150 | |
| | | | C _L = 150 pF to 2500 pF, | MBAUD = GND | 4 | 30 | |

(1) Test conditions are C1–C4 = 0.1 μF at V_{CC} = 3 V to 5 V.

(2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C.

(3) Pulse skew is defined as |t_{PLH} – t_{PHL}| of each channel of the same device.

RECEIVER SECTION ELECTRICAL CHARACTERISTICS⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see [Figure 5](#))

| PARAMETER | TEST CONDITIONS | MIN | TYP ⁽²⁾ | MAX | UNIT |
|--|---------------------------------|-----------------------|-----------------------|-----|------|
| V _{OH} High-level output voltage | I _{OH} = –1 mA | V _{CC} – 0.6 | V _{CC} – 0.1 | | V |
| V _{OL} Low-level output voltage | I _{OL} = 1 mA | | | 0.4 | V |
| V _{IT+} Positive-going input threshold voltage | V _{CC} = 3.3 V | | 1.5 | 2.4 | V |
| | V _{CC} = 5 V | | 2 | 2.4 | |
| V _{IT–} Negative-going input threshold voltage | V _{CC} = 3.3 V | 0.6 | 1.1 | | V |
| | V _{CC} = 5 V | 0.8 | 1.5 | | |
| V _{hys} Input hysteresis (V _{IT+} – V _{IT–}) | | | 0.5 | | V |
| I _{oz} Output leakage current | $\overline{\text{EN}} = V_{CC}$ | | ±0.05 | ±10 | μA |
| r _i Input resistance | V _I = ±3 V to ±25 V | 3 | 5 | 7 | kΩ |

(1) Test conditions are C1–C4 = 0.1 mF at V_{CC} = 3 V to 5 V.

(2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C.

RECEIVER SECTION SWITCHING CHARACTERISTICS⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

| PARAMETER | TEST CONDITIONS | TYP ⁽²⁾ | UNIT |
|--|--|--------------------|------|
| t _{PLH} Propagation delay time, low- to high-level output | C _L = 150 pF, See Figure 3 | 150 | ns |
| t _{PHL} Propagation delay time, high- to low-level output | C _L = 150 pF, See Figure 3 | 150 | ns |
| t _{en} Output enable time | C _L = 150 pF, R _L = 3 kΩ, See Figure 4 | 2.6 | μs |
| t _{dis} Output disable time | C _L = 150 pF, R _L = 3 kΩ, See Figure 4 | 2.4 | μs |
| t _{sk(p)} Pulse skew ⁽³⁾ | See Figure 3 | 50 | ns |

(1) Test conditions are C1–C4 = 0.1 μF at V_{CC} = 3 V to 5 V.

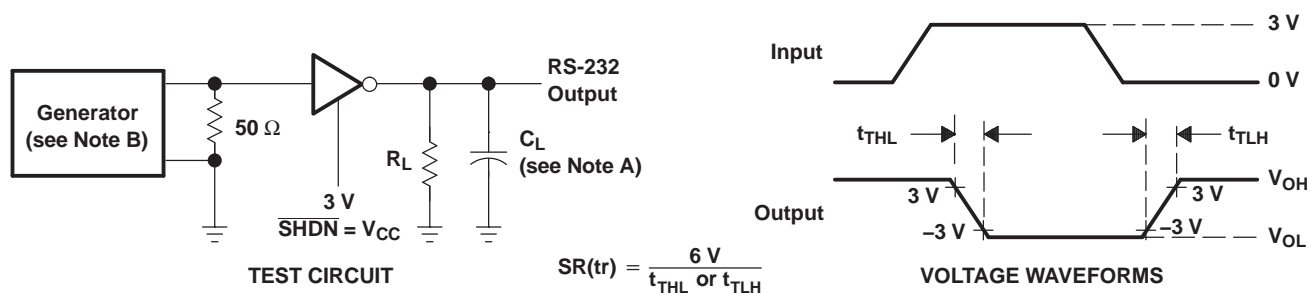
(2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C.

(3) Pulse skew is defined as |t_{PLH} – t_{PHL}| of each channel of the same device.

ESD PROTECTION

| PIN | TEST CONDITIONS | TYP | UNIT |
|-----------|---------------------------------|-----|------|
| DOUT, RIN | IEC61000-4-2, Contact Discharge | ±8 | kV |
| | IEC61000-4-2, Air-Gap Discharge | ±15 | |

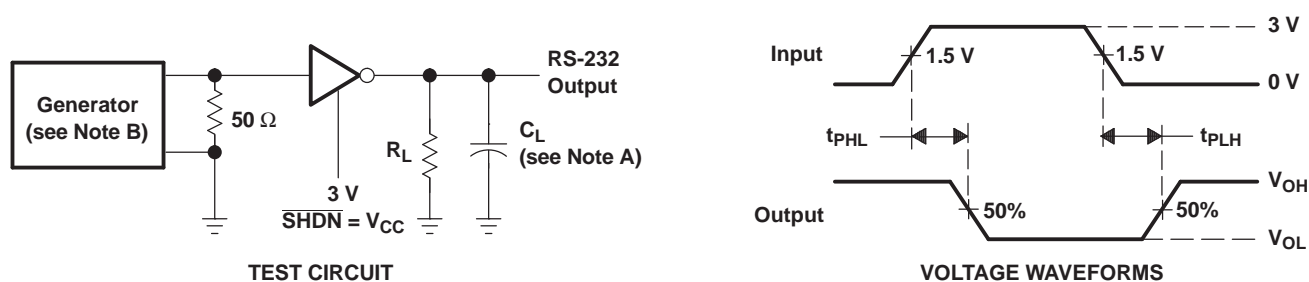
PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: PRR = 250 kbit/s, Z_O = 50 Ω, 50% duty cycle, t_r ≤ 10 ns, t_f ≤ 10 ns.

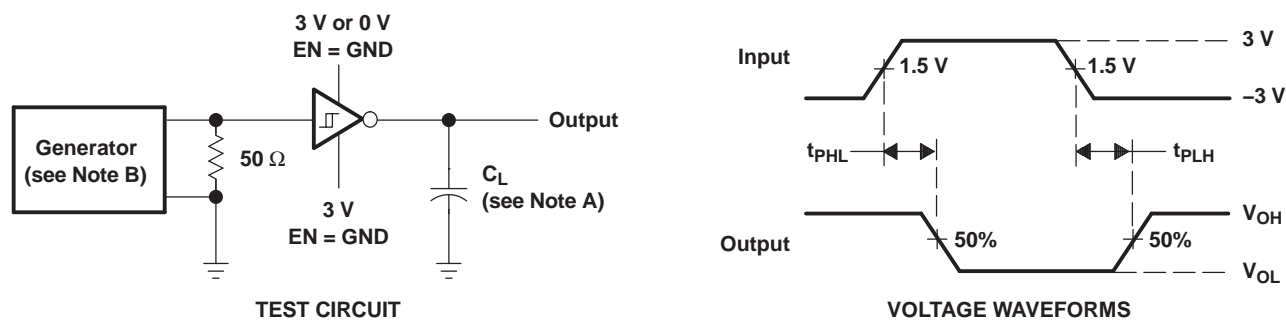
Figure 1. Driver Slew Rate



NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: PRR = 250 kbit/s, Z_O = 50 Ω, 50% duty cycle, t_r ≤ 10 ns, t_f ≤ 10 ns.

Figure 2. Driver Pulse Skew

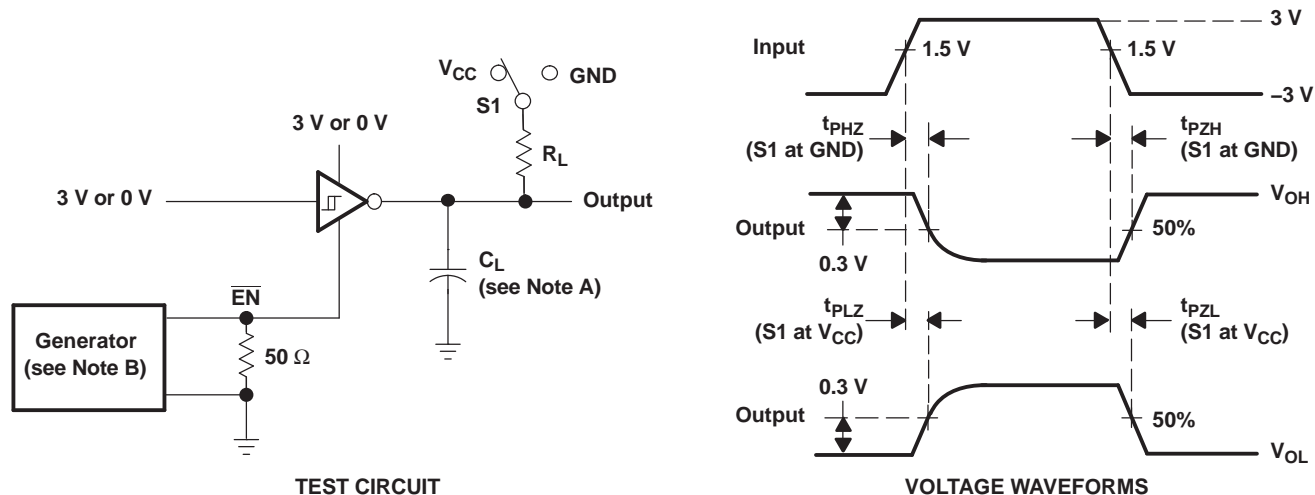


NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: Z_O = 50 Ω, 50% duty cycle, t_r ≤ 10 ns, t_f ≤ 10 ns.

Figure 3. Receiver Propagation Delay Times

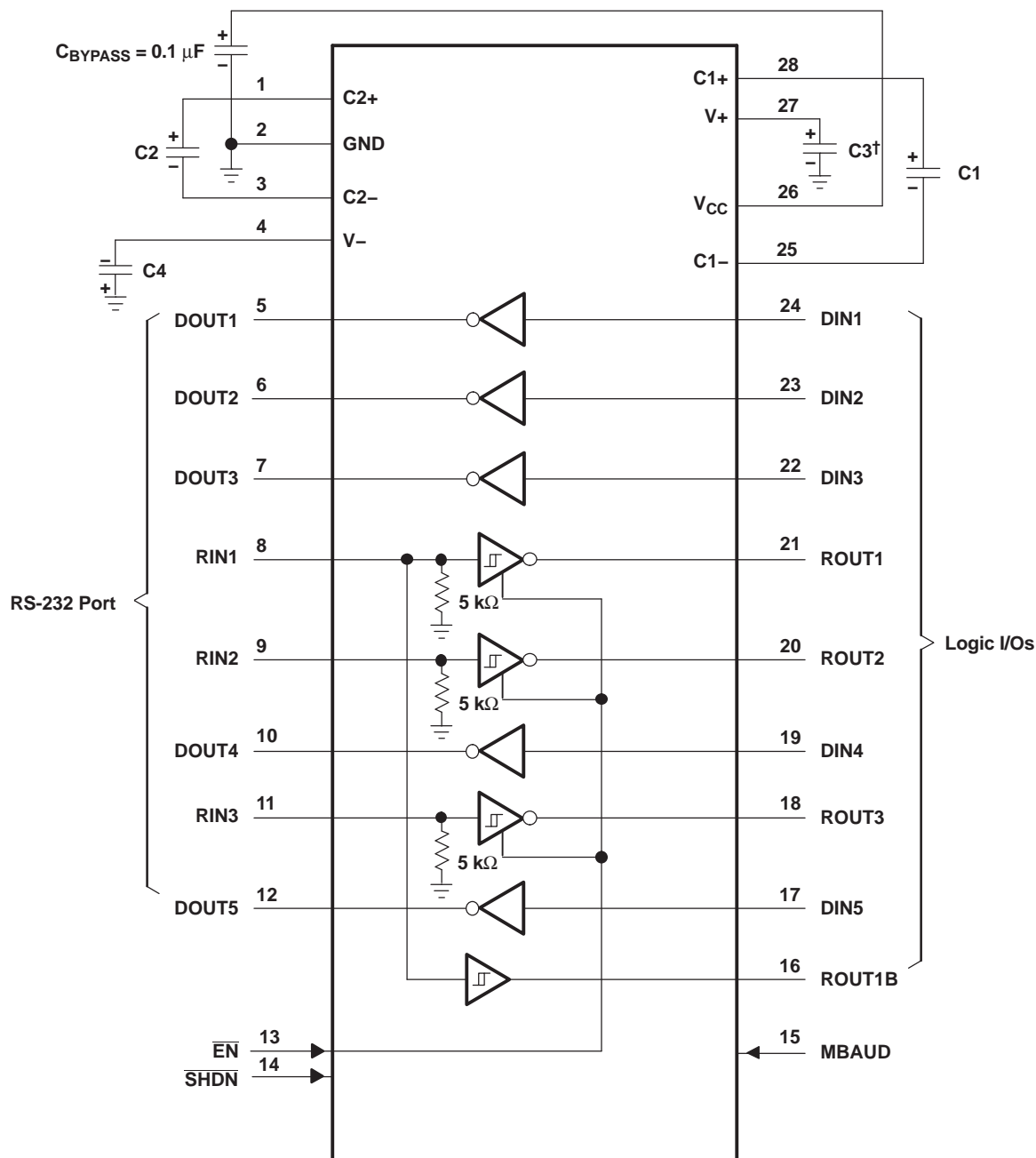
PARAMETER MEASUREMENT INFORMATION (continued)



- NOTES:
- A. C_L includes probe and jig capacitance.
 - B. The pulse generator has the following characteristics: $Z_O = 50\ \Omega$, 50% duty cycle, $t_r \leq 10\ \text{ns}$, $t_f \leq 10\ \text{ns}$.
 - C. t_{PLZ} and t_{PHZ} are the same as t_{dis} .
 - D. t_{PZL} and t_{PZH} are the same as t_{en} .

Figure 4. Receiver Enable and Disable Times

APPLICATION INFORMATION



† C3 can be connected to V_{CC} or GND.

NOTES: A. Resistor values shown are nominal.

B. Nonpolarized ceramic capacitors are acceptable. If polarized tantalum or electrolytic capacitors are used, they should be connected as shown.

V_{CC} vs CAPACITOR VALUES

| V _{CC} | C1 | C2, C3, and C4 |
|-----------------|----------|----------------|
| 3.3 V ± 0.15 V | 0.1 μF | 0.1 μF |
| 3.3 V ± 0.3 V | 0.22 μF | 0.22 μF |
| 5 V ± 0.5 V | 0.047 μF | 0.33 μF |
| 3 V to 5.5 V | 0.22 μF | 1 μF |

Figure 5. Typical Operating Circuit and Capacitor Values

PACKAGING INFORMATION

| Orderable Device | Status (1) | Package Type | Package Drawing | Pins | Package Qty | Eco Plan (2) | Lead finish/ Ball material (6) | MSL Peak Temp (3) | Op Temp (°C) | Device Marking (4/5) | Samples |
|------------------|---------------|--------------|--------------------|------|----------------|-----------------|--------------------------------------|----------------------|--------------|-------------------------|-------------------------|
| TRS3237ECDB | ACTIVE | SSOP | DB | 28 | 50 | RoHS & Green | NIPDAU | Level-1-260C-UNLIM | 0 to 70 | TRS3237EC | Samples |
| TRS3237ECDBR | ACTIVE | SSOP | DB | 28 | 2000 | RoHS & Green | NIPDAU | Level-1-260C-UNLIM | 0 to 70 | TRS3237EC | Samples |
| TRS3237ECDWR | ACTIVE | SOIC | DW | 28 | 1000 | RoHS & Green | NIPDAU | Level-1-260C-UNLIM | 0 to 70 | TRS3237EC | Samples |
| TRS3237ECPWR | ACTIVE | TSSOP | PW | 28 | 2000 | RoHS & Green | NIPDAU | Level-1-260C-UNLIM | 0 to 70 | RS37EC | Samples |
| TRS3237EIDB | ACTIVE | SSOP | DB | 28 | 50 | RoHS & Green | NIPDAU | Level-1-260C-UNLIM | -40 to 85 | TRS3237EI | Samples |
| TRS3237EIDBR | ACTIVE | SSOP | DB | 28 | 2000 | RoHS & Green | NIPDAU | Level-1-260C-UNLIM | -40 to 85 | TRS3237EI | Samples |
| TRS3237EIDWR | ACTIVE | SOIC | DW | 28 | 1000 | RoHS & Green | NIPDAU | Level-1-260C-UNLIM | -40 to 85 | TRS3237EI | Samples |
| TRS3237EIPWR | ACTIVE | TSSOP | PW | 28 | 2000 | RoHS & Green | NIPDAU | Level-1-260C-UNLIM | -40 to 85 | RS37EI | Samples |

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

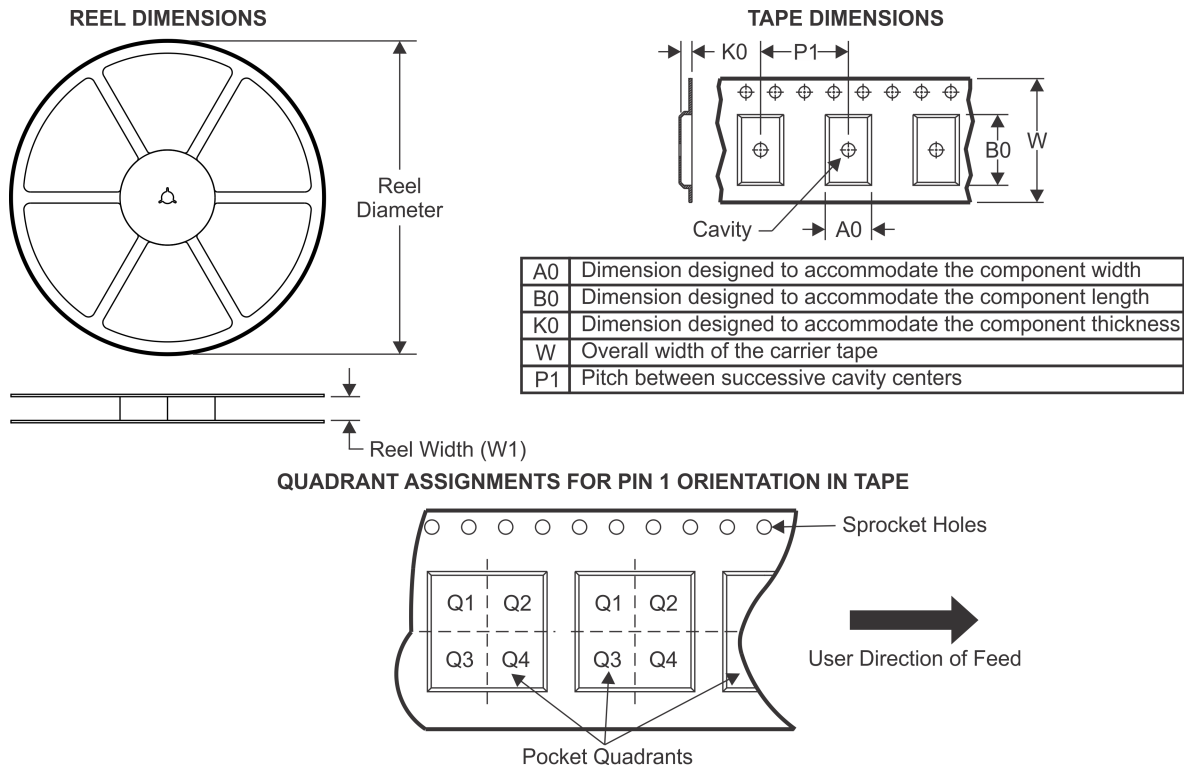
(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

⁽⁶⁾ Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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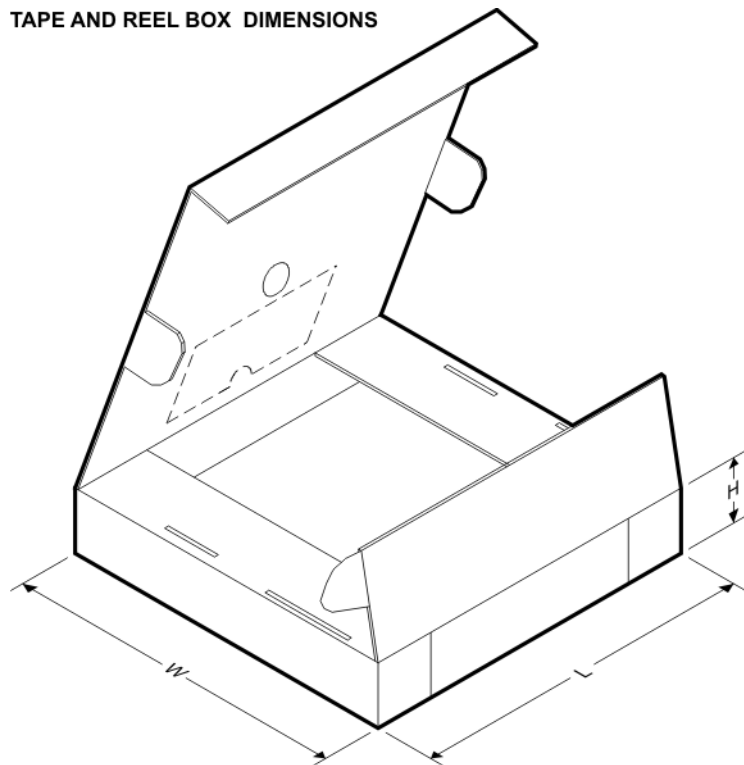
TAPE AND REEL INFORMATION



*All dimensions are nominal

| Device | Package Type | Package Drawing | Pins | SPQ | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|--------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| TRS3237ECDBR | SSOP | DB | 28 | 2000 | 330.0 | 16.4 | 8.2 | 10.5 | 2.5 | 12.0 | 16.0 | Q1 |
| TRS3237ECDWR | SOIC | DW | 28 | 1000 | 330.0 | 32.4 | 11.35 | 18.67 | 3.1 | 16.0 | 32.0 | Q1 |
| TRS3237ECPWR | TSSOP | PW | 28 | 2000 | 330.0 | 16.4 | 6.9 | 10.2 | 1.8 | 12.0 | 16.0 | Q1 |
| TRS3237EIDBR | SSOP | DB | 28 | 2000 | 330.0 | 16.4 | 8.2 | 10.5 | 2.5 | 12.0 | 16.0 | Q1 |
| TRS3237EIDWR | SOIC | DW | 28 | 1000 | 330.0 | 32.4 | 11.35 | 18.67 | 3.1 | 16.0 | 32.0 | Q1 |
| TRS3237EIPWR | TSSOP | PW | 28 | 2000 | 330.0 | 16.4 | 6.9 | 10.2 | 1.8 | 12.0 | 16.0 | Q1 |

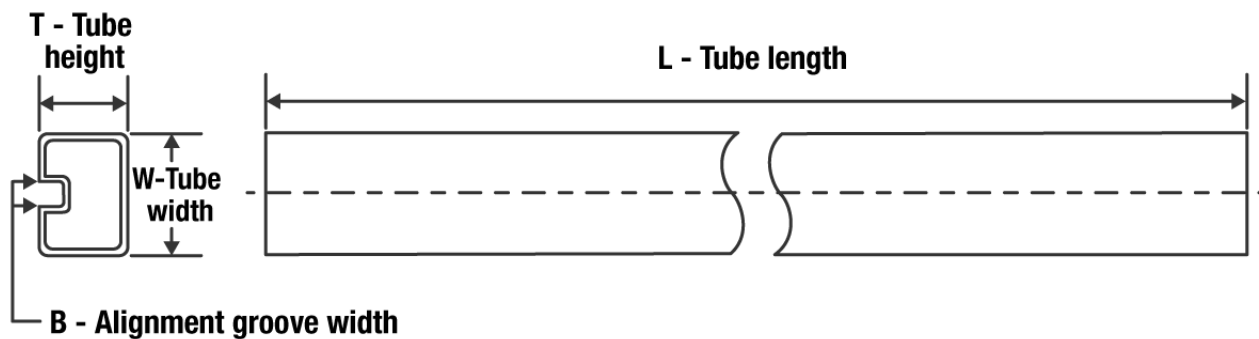
TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

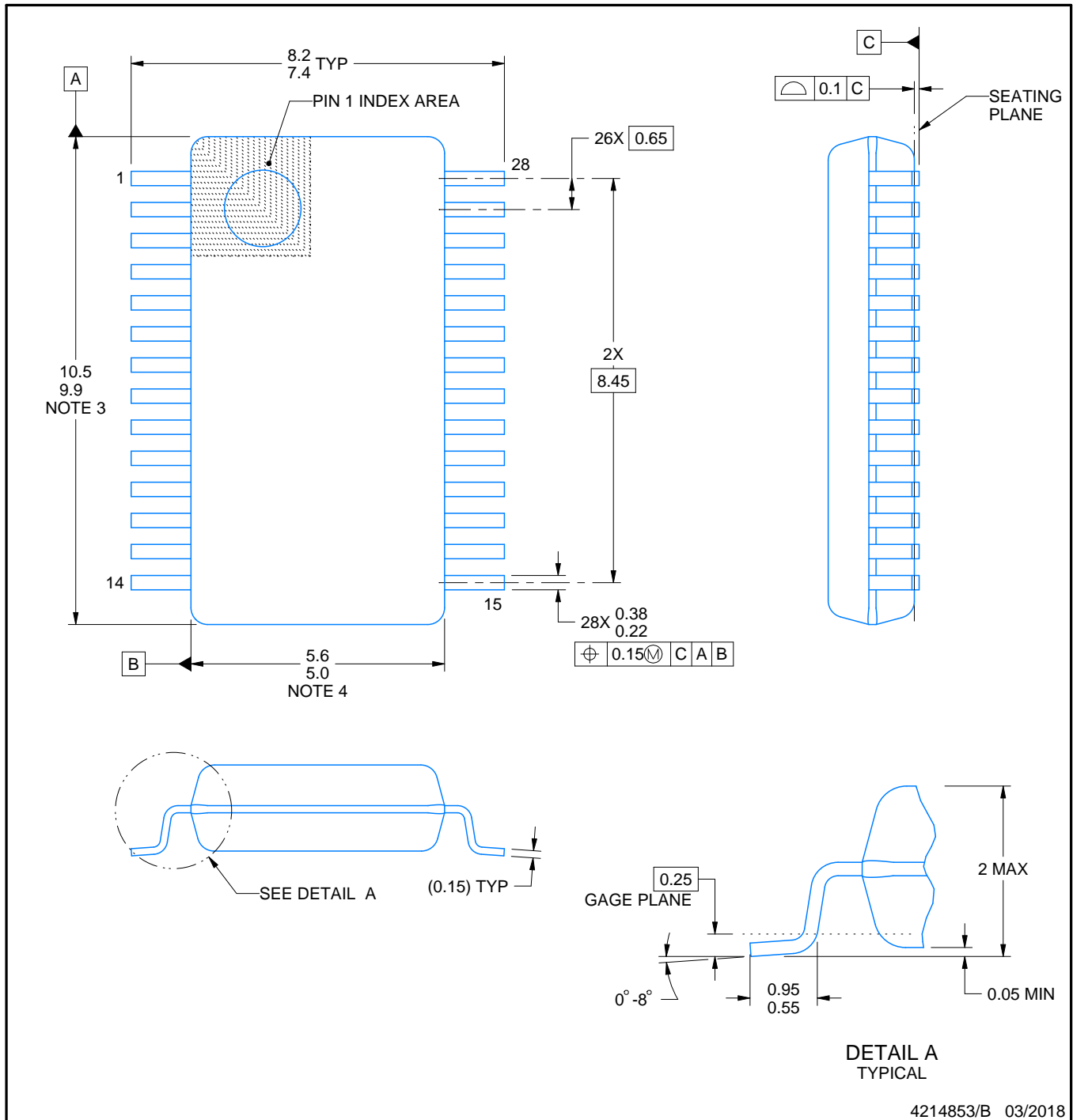
| Device | Package Type | Package Drawing | Pins | SPQ | Length (mm) | Width (mm) | Height (mm) |
|--------------|--------------|-----------------|------|------|-------------|------------|-------------|
| TRS3237ECDBR | SSOP | DB | 28 | 2000 | 853.0 | 449.0 | 35.0 |
| TRS3237ECDWR | SOIC | DW | 28 | 1000 | 350.0 | 350.0 | 66.0 |
| TRS3237ECPWR | TSSOP | PW | 28 | 2000 | 853.0 | 449.0 | 35.0 |
| TRS3237EIDBR | SSOP | DB | 28 | 2000 | 853.0 | 449.0 | 35.0 |
| TRS3237EIDWR | SOIC | DW | 28 | 1000 | 350.0 | 350.0 | 66.0 |
| TRS3237EIPWR | TSSOP | PW | 28 | 2000 | 853.0 | 449.0 | 35.0 |

TUBE



*All dimensions are nominal

| Device | Package Name | Package Type | Pins | SPQ | L (mm) | W (mm) | T (μm) | B (mm) |
|-------------|--------------|--------------|------|-----|--------|--------|--------|--------|
| TRS3237ECDB | DB | SSOP | 28 | 50 | 530 | 10.5 | 4000 | 4.1 |
| TRS3237EIDB | DB | SSOP | 28 | 50 | 530 | 10.5 | 4000 | 4.1 |



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NOTES:

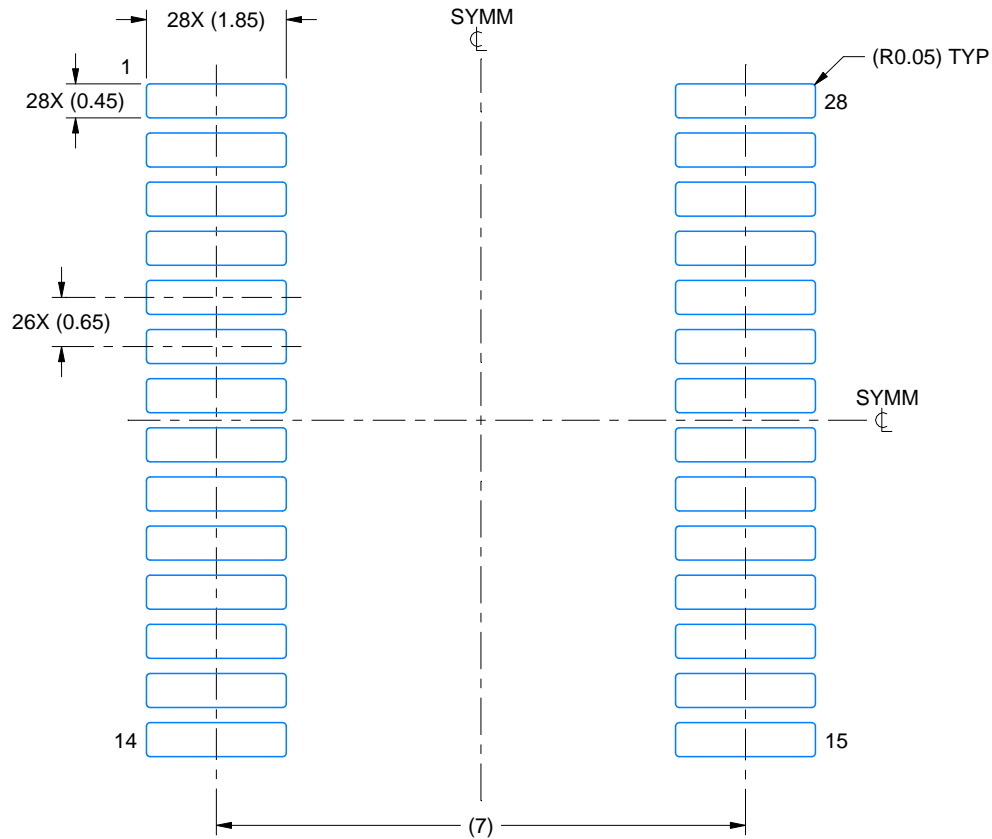
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm per side.
4. This dimension does not include interlead flash. Interlead flash shall not exceed 0.25 mm per side.
5. Reference JEDEC registration MO-150.

EXAMPLE BOARD LAYOUT

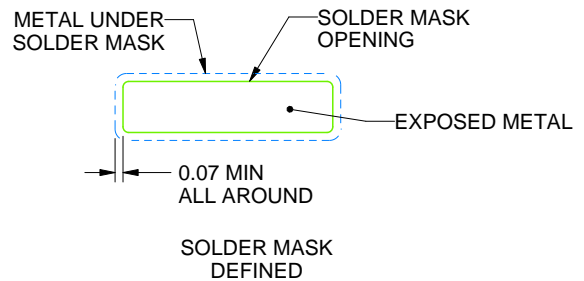
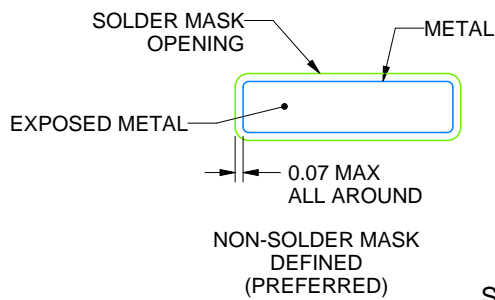
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SSOP - 2 mm max height

SMALL OUTLINE PACKAGE



LAND PATTERN EXAMPLE
EXPOSED METAL SHOWN
SCALE: 10X



SOLDER MASK DETAILS

4214853/B 03/2018

NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.

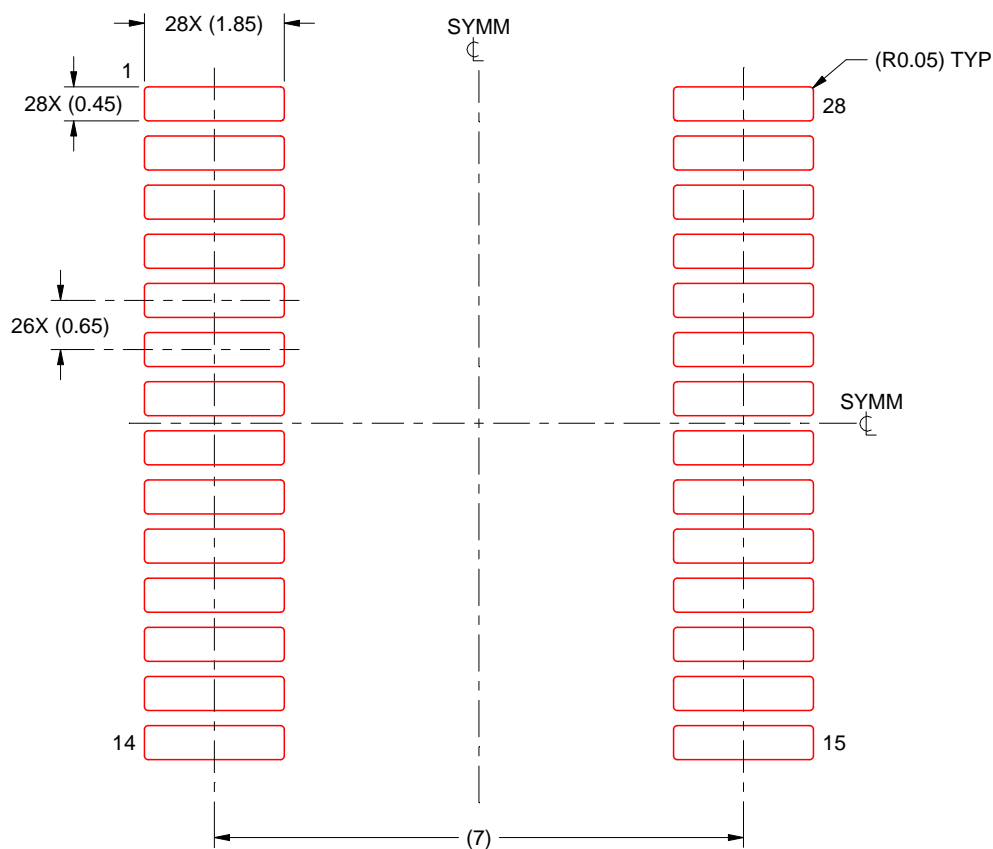
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

EXAMPLE STENCIL DESIGN

DB0028A

SSOP - 2 mm max height

SMALL OUTLINE PACKAGE



SOLDER PASTE EXAMPLE
BASED ON 0.125 mm THICK STENCIL
SCALE: 10X

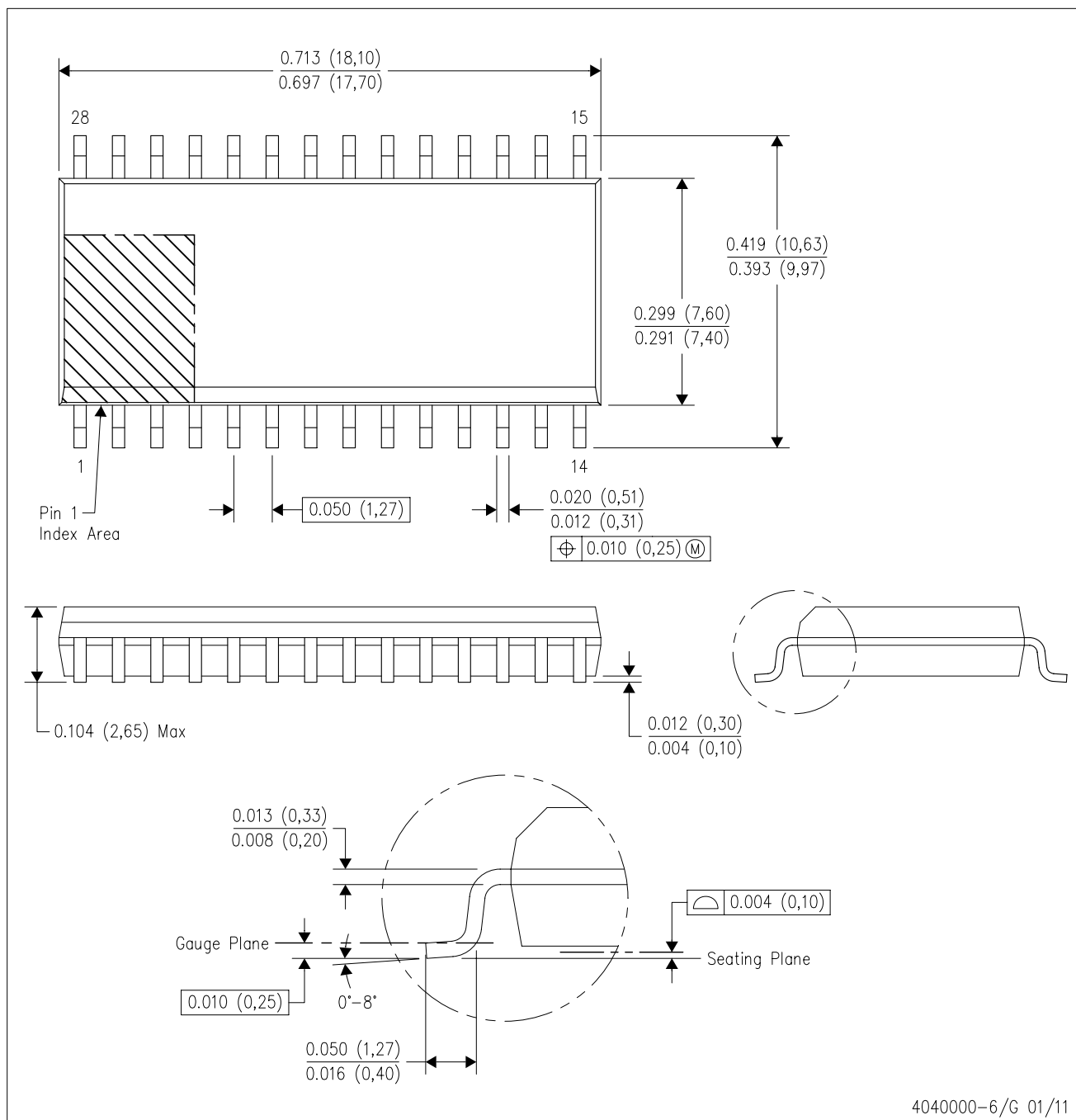
4214853/B 03/2018

NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.

DW (R-PDSO-G28)

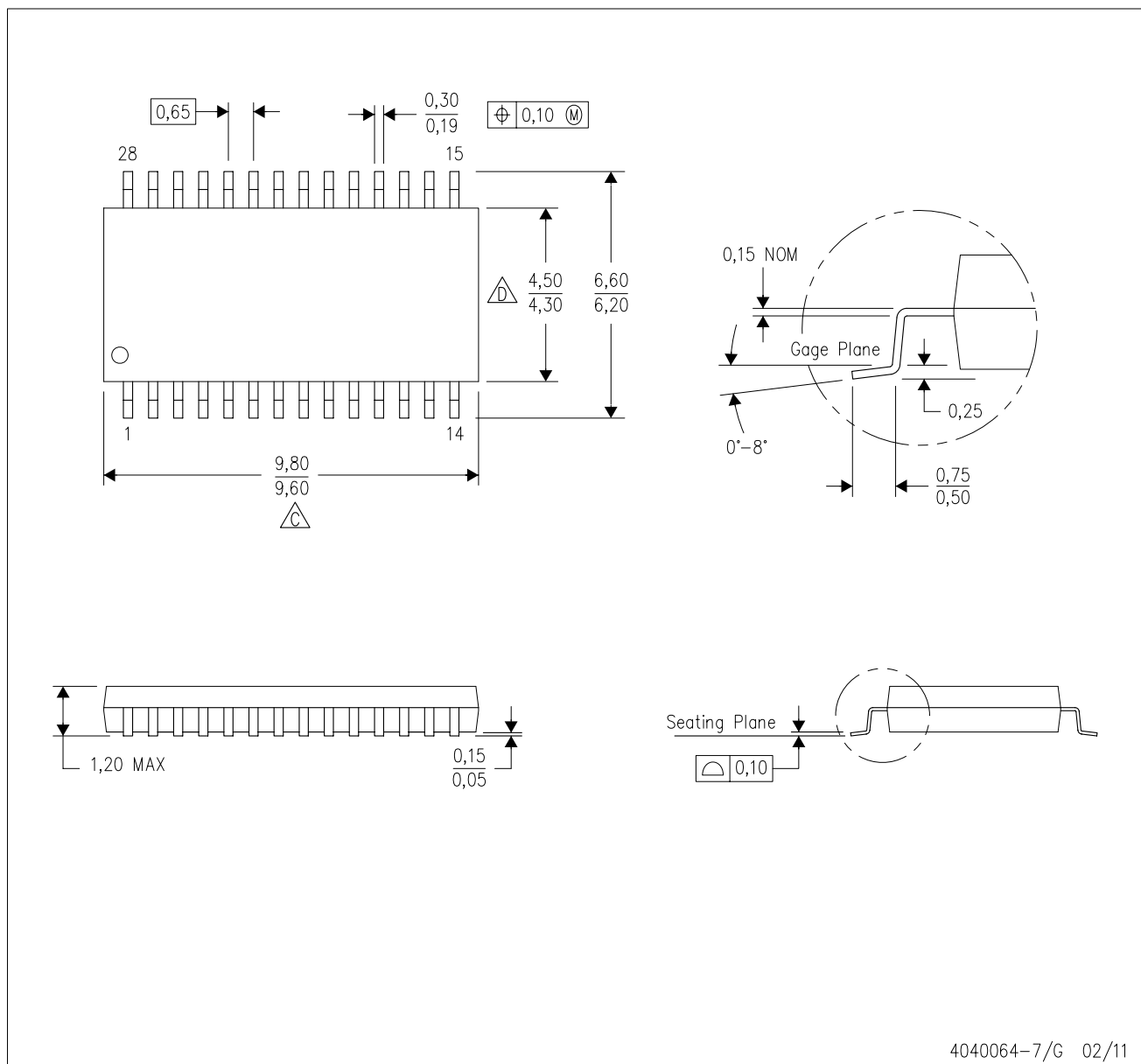
PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in inches (millimeters). Dimensioning and tolerancing per ASME Y14.5M-1994.
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
 - D. Falls within JEDEC MS-013 variation AE.

PW (R-PDSO-G28)

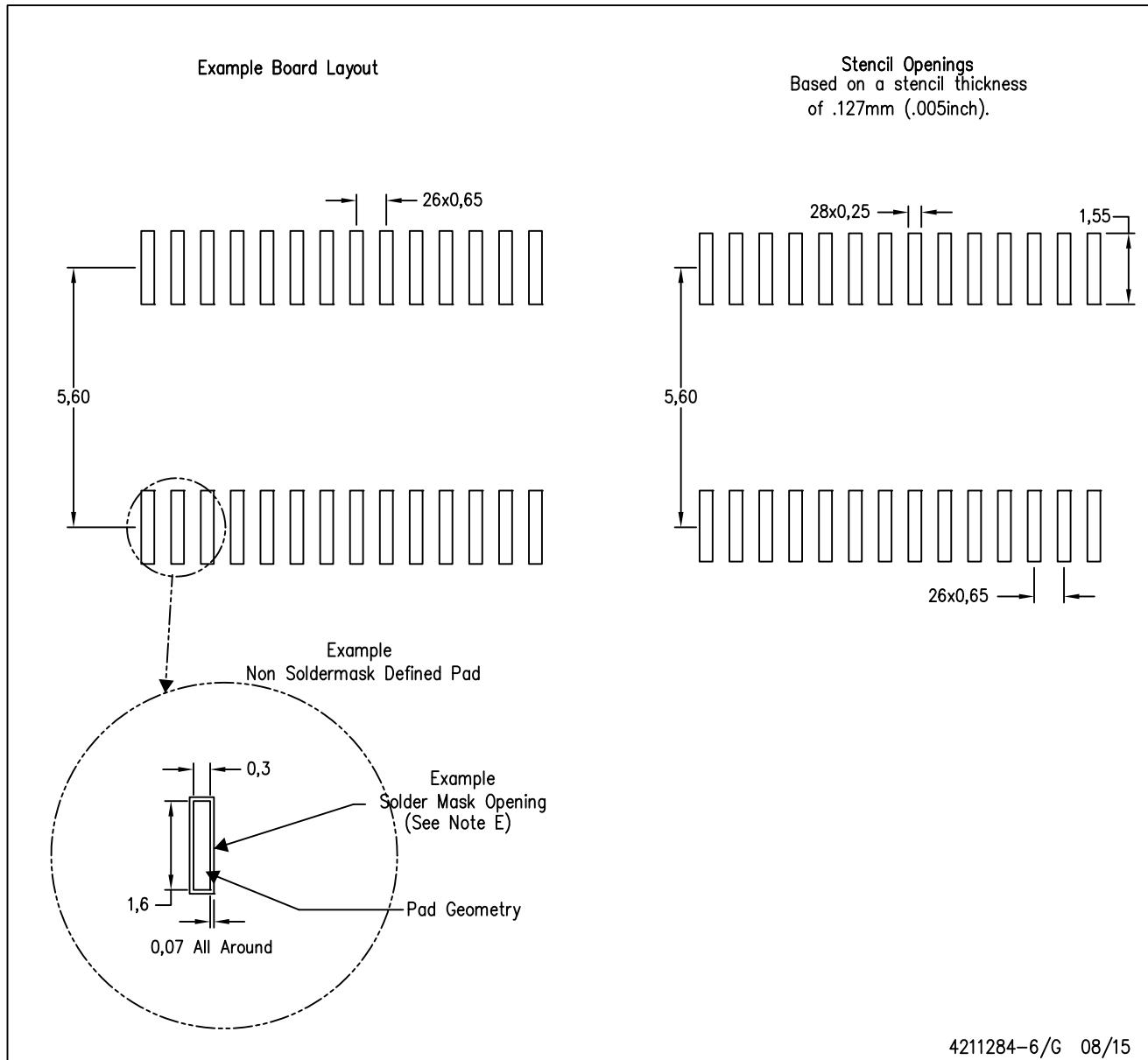
PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
 - B. This drawing is subject to change without notice.
 - C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0,15 each side.
 - D. Body width does not include interlead flash. Interlead flash shall not exceed 0,25 each side.
 - E. Falls within JEDEC MO-153

PW (R-PDSO-G28)

PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Publication IPC-7351 is recommended for alternate design.
 - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
 - E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

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